

Notice of References Cited

Application/Control No.

09/805,138

Applicant(s)/Patent Under
Reexamination
MOORE ET AL.

Examiner

Yolanda Wilson

Art Unit

2184

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	U	Brennan et al. Low Power Methodology and Design Techniques for Processor Design. ACM. August 10-12, 1998. pages 268-273.
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